

#1 1/2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of: : Date: November 7, 2000

Jefferson W. Hall et al. :

Patent No.: 5,859,768 : Group Art Unit: 2838

Issued: January 12, 2000 : Examiner: R. Patel

For: POWER CONVERSION INTEGRATED CIRCUIT AND METHOD FOR
PROGRAMMING

JC903 U.S. PTO
- 09/709893
11/13/00

DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

SIR:

It is respectfully requested that the art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed art is enclosed herewith.

No representation is made or intended that the listed art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better art than that listed is available.

Respectfully submitted,

Jefferson W. Hall et al.

Michael T. Wallace

Semiconductor Components Industries, LLC
Patent Administration Dept - MD A230
P.O. Box 62890
Phoenix, AZ 85082-2890

Michael T. Wallace
Agent for Applicant(s)
Reg. No.: 45,420
Telephone No.: (602) 244-5416
Facsimile No.: (602) 244-5601